Claims

[c1]   1. A converting circuit, for preventing wrong error cor-
rection code from occurring due to an error correction
rule during data reading operation, comprising:
a flash memory controller, comprising an error correc-
tion code converting circuit connected to a byte error
correction rule;
an error correction code area, connected to said flash
memory controller, for storing error correction codes
generated by said flash memory controller; and
a data area, connected to said flash memory controller,
for storing data transmitted by said flash memory,
wherein when said flash memory controller writes all 0 x
FF data into said flash memory, said byte error correction
rule generates a set of correct error correction codes for
said data area and said error correction code converting
circuit converts said set of correct error correction codes
into 0 x FF error correction codes, and values stored in
said data area and error correction code area of said
flash memory are converted into 0 x FF to prevent wrong
error correction code from occurring during data reading
operation when said error correction codes are not com-
pletely 0 x FF.

[c2]   2. The converting circuit according to claim 1, wherein said error correction code converting circuit comprises a positive converting circuit and a negative converting circuit.

[c3]   3. The converting circuit according to claim 1, wherein said flash memory controller comprises a buffer.